

**LSI Logic**  
**FAX**

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**MAY 31 2005**

**LSI Logic Corporation  
Legal Department – IP  
1621 Barber Lane, MS D-106  
Milpitas, CA 95035**

**Date** May 31, 2005  
**Number of pages including cover sheet** 2

**To:** US PTO  
**Mail Stop Status Request**  
**TC 2825**  
**Fax No.** 703-872-9306  
**Phone No.**  
**CC:**

**From:** Connie del Castillo  
**Intellectual Property Paralegal**  
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**REMARKS:**

<input type="checkbox"/> Urgent	<input type="checkbox"/> For your review	<input checked="" type="checkbox"/> Reply ASAP	<input type="checkbox"/> Please comment
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**Application Number:** 10/643,463  
**Filing date:** August 19, 2003  
**First named inventor:** Jeff Brown  
**Attorney docket number:** 03-0058

Transmitted herewith for filing via facsimile:

- Status Request Letter

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Connie del Castillo

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Jeff Brown

**Serial No. :** 10/643,463

**Filed :** August 19, 2003

For : Type Configurable Memory  
Methodology For Use With Metal  
Programmable Devices

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Group Art Unit: 2825

Examiner:

**Atty Docket :** / 03-0058

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## **STATUS REQUEST**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

Respectfully submitted,

Hogg

Sandeep Jaggi, Ph: [+1] 408-433-7191

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Date: 5/4/03

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